

A liquid crystal display having an electrode pad for compensating for differences in resistance of electrode links. A pad portion in contact with a driving circuit includes a transparent electrode pattern having a length that depends on the length of an associated electrode link that is connected between the pad portion and a corresponding signal line at a pixel area on which a plurality of liquid crystal cells are arranged. Accordingly, resistance differences that depend on the length of the electrode links are compensated for using electrode pads, thereby making signal conductors with substantially equal resistances.